

**IN THE CLAIMS:**

This listing of claims will replace all prior versions, and listings, of claims in the application:

**Listing of Claims:**

1. – 14 (Cancelled)

15. (Original) A method of manufacturing a semiconductor device, comprising the steps of:

(a) providing a wiring substrate having a plurality of second electrode pads formed over a main surface thereof;

(b) mounting a semiconductor chip over the wiring substrate, the semiconductor chip having over a main surface thereof a plurality of first electrode pads formed as exposed areas of a metallic film which contains aluminum (Al) as a main component; and

(c) connecting the first electrode pads and the second electrode pads with each other through electrically conductive wires which contain gold (Au) as a main component,

the step (c) comprising the steps of:

(c1) melting over the first electrode pads one ends of the electrically conductive wires and bonding the resulting melted balls over the first electrode pads;

(c2) bonding opposite ends of the electrically conductive wires over the second electrode pads; and

(c3) forming the electrically conductive wires having ball portions formed over the first electrode pads, bonded portions formed over the second electrode pads, and wire portions for connecting the ball portions and the bonded portions with each other, the ball portions being bonded to the first electrode pads through an alloy layer of Al and Au,

(d) wherein palladium (Pd) is contained in the electrically conductive wires, and

(e) wherein the step (c1) is carried out in a state in which the temperature of the main surface of the semiconductor chip is not higher than 200°C.

16. (Original) A method according to claim 15, wherein the wiring substrate is formed of a glass fabric-based epoxy resin.

17. (Original) A method according to claim 15, further comprising a step of sealing the semiconductor chip and the electrically conductive wire with a molding resin.

18. (Original) A method according to claim 15, wherein the wiring substrate is constituted by a resin tape.

19. (Original) A method according to claim 15, wherein the distance between central positions of the adjacent first electrode pads is not longer than 50  $\mu\text{m}$ .

20. (Original) A method according to claim 15, wherein, the distance between central positions of the adjacent first electrode pads is shorter than 65 $\mu$ m.

21. (Original) A method according to claim 15, wherein, the diameter of a maximum external form of each of the ball portions is smaller than 55  $\mu$ m.

22. (Original) A method according to claim 15, wherein, the diameter of a maximum external form of each of the ball portions is not larger than 40  $\mu$ m.

23. (Original) A method according to claim 15, wherein, the diameter of each of the wire portions is not larger than 25  $\mu$ m.

24. (Original) A method according to claim 15, wherein, the diameter of each of the wire portions is not larger than 20  $\mu$ m.

25. (Original) A method according to claim 15, wherein, the thickness of the metallic film is not smaller than 1000 nm.

26. (Original) A method according to claim 15, wherein, the thickness of the metallic film is not larger than 400 nm.

27. (Original) A method according to claim 15, wherein, the concentration of the palladium (Pd) contained in the electrically conductive wires is approximately 1 wt.%.